# Rocktech Displays Limited



Module P/N: RK070CU01H-CTG
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Version: 1.0

Description: 7.0 inch TFT 1024\*600 pixels with LED

backlight ,All viewing angle, Capacitive

touch Panel, 800 nits luminance

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# **Revision History**

Date	Rev.	Page	Description
2018-08-08	1.0	All	First Issue



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## 1. General Features

Item	Spec	Remark
Display Mode	Normally Black transmissive	
Viewing Direction	Free	IPS
Input Signals	LVDS 6/8 bits	
Outside Dimensions(mm)	164.9 (W) x100(H) x7.6 (D)	With CTP
Active Area(mm)	154.21(W)×85.92(H)	
Number of Pixels	1024(RGB)×600	
Dot Pitch(mm)	0.1506 (W) x 0.1432 (H)	
Pixel Arrangement	RGB Vertical stripes	
Drive IC	-	
CTP Drive IC	GT911	



# 2. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Sym.	Min.	Тур.	Max.	Unit	Remark
	VDD	-0.3	-	3.96	V	
Dower for Circuit Driving	AVDD	-0.5	-	14.85	V	
Power for Circuit Driving	VGH	-0.3	-	40	V	
	VGL	-20	-	0.3	V	
Storage Humidity	H <sub>ST</sub>	10	-		%RH	
Storage Temperature	T <sub>ST</sub>	-30	-	80	$^{\circ}$	At
Operating Ambient Humidity	Нор	10	-		%RH	<b>25±5</b> ℃
Operating Ambient temperature	T <sub>OP</sub>	-20	-	70	$^{\circ}\!\mathbb{C}$	



# 3. Electrical Specification

### 3.1 Driving TFT LCD Panel

Item	Sym.	Min	Тур.	Max	Unit	Note
Supply Voltage	DVDD	3.0	3.3	3.6	V	
	VGH	17.0	18.0	19.0	V	
	VGL	-6.6	-6.0	-5.4	V	
	AVDD	9.4	9.6	9.8	V	
	VCOM	3.6	3.8	4.0	V	

### 3.2 Driving LED Backlight

Item	Sym.	Min	Тур.	Max	Unit	Note
Backlight driving voltage	VF	9.0	9.6	10.2	V	
Backlight driving current	lF	1	270	-	mA	
Backlight Power Consumption	WBL	1	2592	1	mW	
Life Time	1	1	50,000	-		Note 1

Note 1: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



## 3.3 Power Consumption

Item	Sym.	Condition	Min	Тур.	Max	Unit	Note
	IVGH	VGH=18V	1	0.23	1.0	mA	
	IVGL	VGL=-6V	-	0.47	1.0	mA	
Current of Power Supply	IVDD	VDD=3.3V	-	20	30	mA	
	IAVDD	AVDD=9.6V	1	17	30	mA	
	IVCOM	VCOM=3.8V	-	3.0	5.0	mA	

## 3.4 Driving CTP

Item	Value
Working Voltage	DC 3.3V
Interface	I2C
Support Touch	5 Points



## **4.Optical Specifications**

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to  $0^{\circ}$ .

Maria	Curren		Values		11:4	Nata	
ltem	Sym.	Min.	Тур.	Max.	Unit	Note	
1)Contrast Ratio	C/R	-	800	-		FIG.1	
2)Module Luminance	L	700	800	-	cd/m <sup>2</sup>	After CTP	
3)Response time	Tr+Tf	-	35	-	ms	FIG.2	
	θτ	-	85	-			
4)\/ioving Angle	θв	-	85	-	Dograda	FIC 2	
4)Viewing Angle	θL	-	85	-	Degree	FIG.3	
	$\theta_{R}$	-	85	-			
	Wx	0.27	0.31	0.35			
	Wy	0.29	0.33	0.37			
	Rx	-	-	-			
5)Ohnomontioite	Ry	-	-	-			
5)Chromaticity	Gx	-	-	-			
	Gy	-	-	-			
	Вх	-	-	-			
	Ву	-	-	-			



### **♦ Measurement System**

#### Notes:

1. Contrast Ratio(CR) is defined mathematically as:

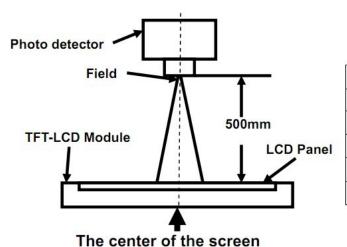
#### Surface Luminance with all white pixels

Contrast Ratio = -----

### Surface Luminance with all black pixels

- 2. Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.
- 3. Response time is the time required for the display to transition from white to black (Rising Time, Tr) and from black to white (Falling Time, Tf). For additional information see FIG 2.
- 4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

### FIG. 1 Optical Characteristic Measurement Equipment and Method



Item	Photo detector	Field
Contrast Ratio		
Luminance	SR-3A	1°
Chromaticity	SK-3A	1
Lum Uniformity		
Response Time	BM-7A	2°

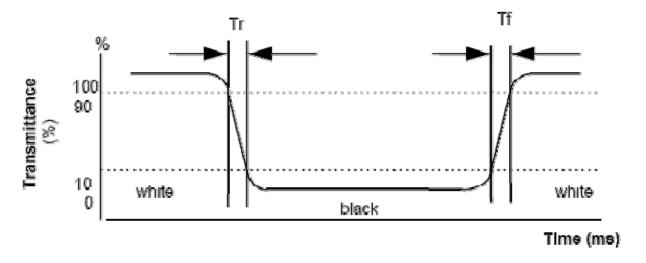


### FIG. 2 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

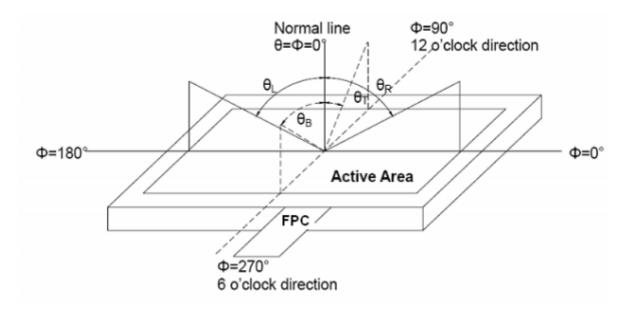
Response Time = Rising Time(Tr) + Falling Time(Tf)

- Rising Time(Tr): Full White 90% → Full White 10% Transmittance.
- Falling Time(Tf): Full White 10% → Full White 90% Transmittance.



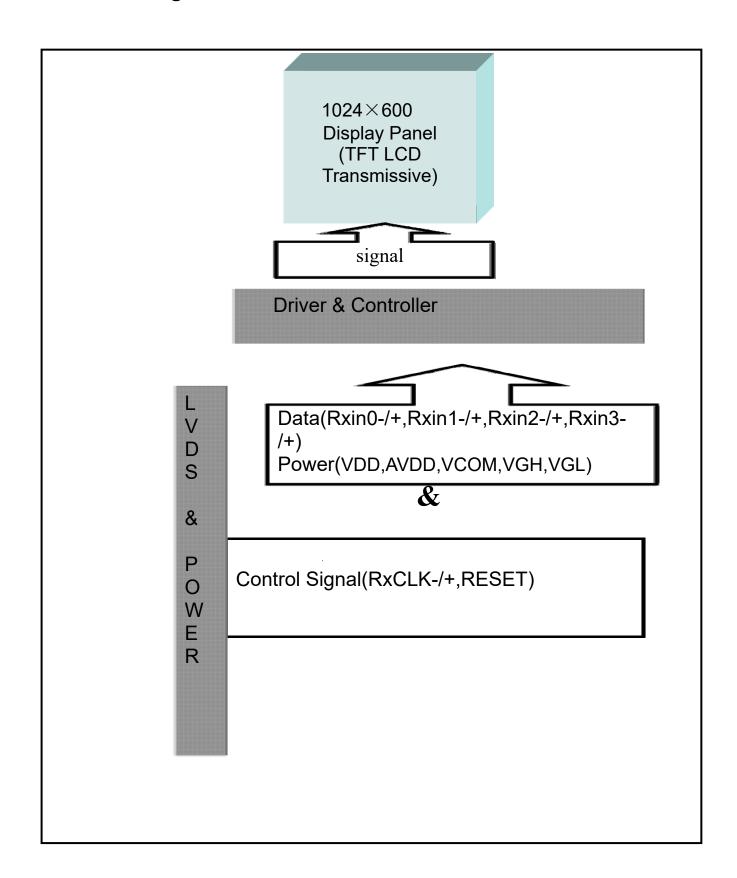
## FIG. 3 The definition of Viewing Angle

Use Fig. 1(Test Procedure) under Measurement System to measure the contrast from the measuring direction specified by the conditions as the following figure.





## 5. Block Diagram





# 6. Pin Description

### 6.1 TFT LCD Panel

Item	Terminal	I/O	Functions	
1	VCOM	Р	Common Voltage	
2	VDD	Р	Power Supply, 3.3V	
3	VDD	Р	Power Supply, 3.3V	
4	NC	-	No connection	
5	Reset	1	Global Reset Pin	
6	STBYB	ı	Standby Mode,Normally pulled high STBYB=1,Normal Operation STBYB=0,Timing Controller,source driver will turn off,all output are High-Z	
7	GND	Р	Ground	
8	Rxin0-	I	-LVDS Differential Data Input	
9	Rxin0+	I	+LVDS Differential Data Input	
10	GND	Р	Ground	
11	Rxin1-	I	-LVDS Differential Data Input	
12	Rxin1+	I	+LVDS Differential Data Input	
13	GND	Р	Ground	
14	Rxin2-	I	-LVDS Differential Data Input	
15	Rxin2+	I	+LVDS Differential Data Input	
16	GND	Р	Ground	
17	RxCLK-	I	-LVDS Differential Data Input	
18	RxCLK+	I	+LVDS Differential Data Input	
19	GND	Р	Ground	
20	Rxin3-	I	-LVDS Differential Data Input	
21	Rxin3+	I	+LVDS Differential Data Input	
22	GND	Р	Ground	
23	NC	-	No connection	
24	NC	-	No connection	
25	GND	Р	Ground	
26,27	NC	-	No connection	
28	SELB	I	6bit/8bit mode select	Note1
29	AVDD	Р	Power for Analog Circuit	
30	GND	Р	Ground	
31	LED-	Р	LED Cathode	



32	LED-	Р	LED Cathode	
33	L/R	I	Horizontal Inversion	Note 2
34	U/D	I	Vertical Inversion	Note 2
35	VGL	Р	Gate OFF Voltage	
36,37	NC	-	No connection	
38	VGH	Р	Gate ON Voltage	
39	LED+	Р	LED Anode	
40	LED+	Р	LED Anode	

I: input, O: output, P: Power

Note 1: If LVDS input data is 6bits,SELB must be set to High;

If LVDS input data is 8bits, SELB must be set to Low.

Note 2: When L/R=0,Set right to left scan direction.

When L/R=1,Set left to right scan direction.

When U/D=0,Set top to bottom scan direction.

When U/D=1,Set bottom to top scan direction.



## **6.2 CTP LCD Pin Description**

Pin	Symbol	Description
1	SDA	I2C data
2	SCL	I2C clock
3	RST	Reset
4	INT	Interrupt
5	VDD	Working voltage 3.3V
6	GND	GND

CTP IC driver source code will be offered separately



# 7. Timing Characteristics

## 7.1. Input Setup Timing Setting

### 7.1.1. Parallel RGB Timing Characteristics

1 ) 1	– r	n٥	$\neg c$	$\Box$
-		ı١٧	Ju	

DL IIIOGE							
DE mode							
Parameter	Cumbal	Value			Linit		
Farameter	Symbol	Min.	Тур.	Max.	Unit		
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz		
Horizontal display area	thd		1024	_	DCLK		
HSYNC period time	th	1114	1344	400	DCLK		
HSYNC blanking	thb+thfp	90	320	376	DCLK		
Vertical display area	tvd		(600)		Н		
VSYNC period time	tv	610	635	800	Н		
VSYNC blanking	tvb+tvfp	10	85	200	Н		

Н	١	/	m	10	10	۵	11	١

HV mode

Horizontal input timing

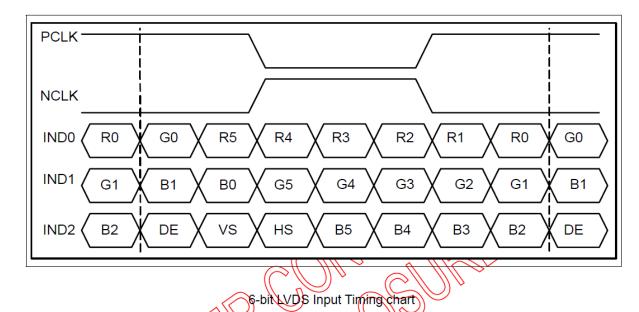
// // ^	$\sim$	11 11		
Symbol		Value		Unit
thd		1024		DCLK
folk	Min.	Тур.	Max.	
S (ICIK)	44.9	51.2	63	Mhz
th	1200	1344	1400	
0		1		
thpw		_		DCLK
		140		DOLK
thbp	160	160	160	
thfp	16	160	216	
	Symbol thd fclk thh	Symbol thd Min. fclk 44.9 th 1200 thpw	Symbol         Value           thd         1024           Min.         Typ.           44.9         51.2           th         1200         1344           thpw         -           140         160         160	Symbol         Value           thd         1024           Min.         Typ.         Max.           fclk         44.9         51.2         63           th         1200         1344         1400           thpw         -         140           thbp         160         160         160

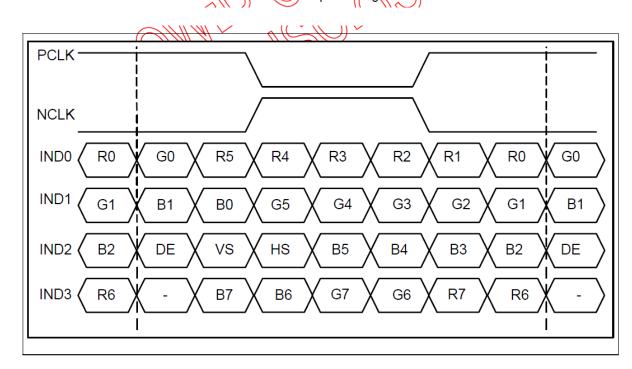
#### HV mode(2)

Vertical input timing						
Parameter	Symbol		Unit			
raiaillelei	Syllibol	Min.	Тур.	Max.	Offic	
Vertical display area	tvd		600		Н	
VSYNC period time	tv	624	635	750	Н	
VSYNC pulse width	tvpw	1	_	20	Н	
VSYNC back porch	tvb	23	23	23	Н	
VSYNC front porch	tvfp	1	12	127	Н	



### 7.1.2. Data Input Format



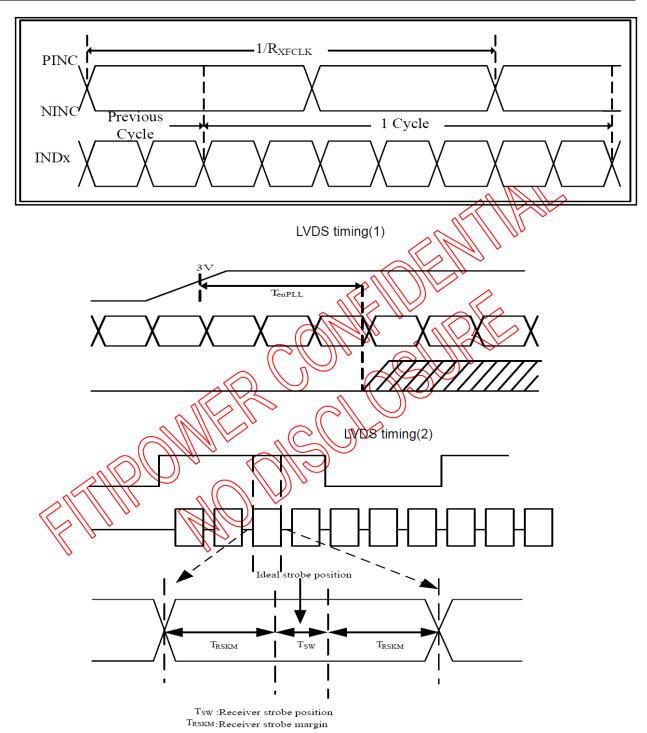


8-bit LVDS Input Timing chart

# 7.2. AC Characteristics

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Clock Frequency	RXFCLK		20	-	71	MHz
Input data skewmargin	TRSKM	NIDL=400mV RXXCM=1.2V RXFCLK=71MHz	500			ps
Clock High Time	MAXCH			4/(7* RxFclk)		ns
Clock Fight Tilde	Marian			4/(/ TXT CER)		ns
Clock low Time	TLVCL			3/(7* RxFCLK)		ns
PLL wake-up-time	TenPLL				150	us





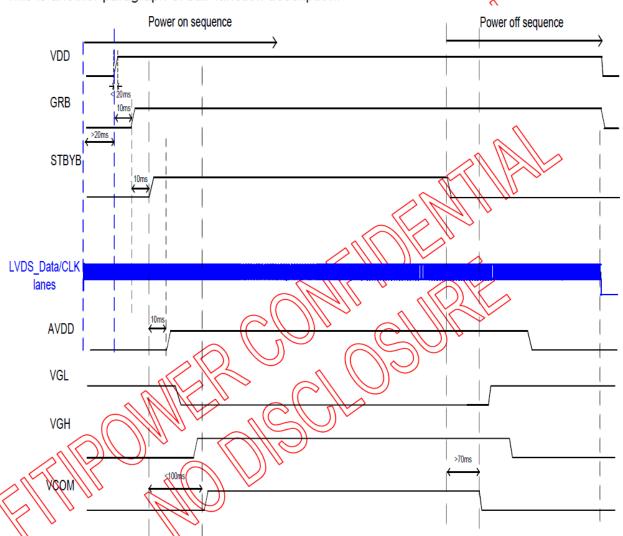
LVDS timing(3)



## 7.3. Power ON/OFF Sequence

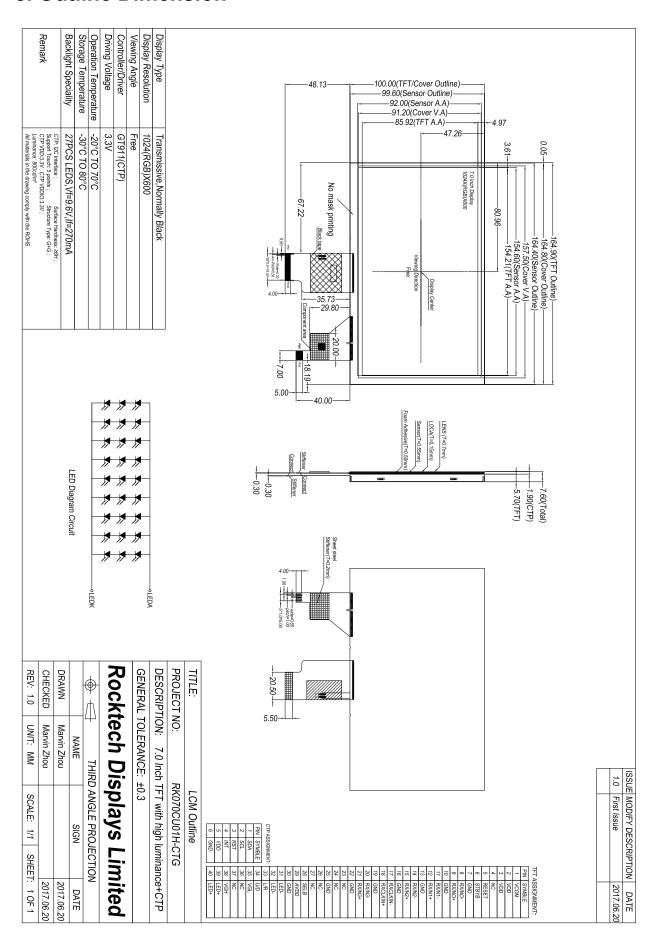
In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

This is another paragraph of sub-function description.





## 8. Outline Dimension





# 9. Reliability and Inspection Standard

No.	Test Item		Test Conditions	Remark	
1	Lligh Tomporature	Storage	80℃, 120Hr	Note	
'	High Temperature	Operation	<b>70</b> ℃, <b>120</b> Hr	Note	
2	Storage		-30℃, 120Hr	Note	
	Low Temperature	Operation	-20℃, 120Hr	Note	
3	High Temperature and High Humidity		40℃, 90%RH, 120Hr	Note	
4	Thermal Cycling Test(No operation)		-20℃ for 30min, 70℃ for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note	
5	Vibration Test(No operation)		Frequency:10~55 HZ; Stroke:1.5 mm;Sweep:10HZ~55HZ~10HZ; 2hours for each direction of X, Y, Z(6 hours for total)		
6	Package Drop Test		Height:60 cm,1 corner, 3 edges, 6 surfaces		
7	Electro Static Discharge		$\pm 2$ KV,Human Body Mode, 100pF/1500 $\Omega$		

#### Note:

- 1) Sample quantity for each test item is 5~10pcs.
- 2) Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



### 10.PRECAUTIONS FOR USING LCD MODULES

### **Handing Precautions**

- (1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
  - Isopropyl alcohol
  - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water
  - Ketone
  - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
  - Do not alter, modify or change the shape of the tab on the metal frame.
  - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
  - Do not damage or modify the pattern writing on the printed circuit board.
  - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal



connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

### **Storage Precautions**

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

#### **Others**

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- -Terminal electrode sections.