Rocktech Displays Limited

LCD Module Specification

Module P/N: RK035BQ01E-CT

Version:　1.0

Description : 3.5 inch TFT 320*240 Pixels with
LED backlight and capacitive touch panel,
All viewing angle,800 nits brightness

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Revision History

Date	Rev.	Page	Description
2022-09-20	1.0	All	First issue

CONTENTS

1. General Features

Item	Spec	Remark
Display Mode	Normally Black transmissive	
Viewing Direction	Free	
Input Signals	RGB 24 bit	
Outside Dimensions	76.9 (W) x63.9(H) x5.16(D)	With TP
Active Area	70.08mm(W)×52.56mm(H)	
Number of Pixels	320(RGB)×240	
Dot Pitch	0.219mm(W)×0.219mm(H)	
Pixel Arrangement	RGB Vertical stripes	
TFT Drive IC	ST7272A	
CTP Drive IC	GT911	

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Sym.	Min.	Typ.	Max.	Unit	Remark
Power for Circuit Driving	Vdd	-0.3	-	4.0	V	
Storage Temperature	T_{ST}	-30	-	80	℃	
Operating Ambient Humidity	H_{OP}	10	-		%RH	
Operating Ambient temperature	T_{OP}	-20	-	70	℃	

3. Electrical Specification

3.1 Driving TFT LCD Panel

Item		Sym.	Min	Typ.	Max	Unit	Note
Power for Circuit Driving		Vdd	3.0	3.3	3.6	V	
Logic Input Voltage	Low Voltage	V_{IL}	0	-	0.3Vdd	V	
	High Voltage	V_{IH}	0.7Vdd	-	Vdd	V	
Logic Output Voltage	Low Voltage	V_{OL}	0	-	0.4	V	
	High Voltage	V_{OH}	Vdd-0.4	-	Vdd	V	

3.2 Driving Backlight

Item	Sym.	Min	Typ.	Max	Unit	Note
Backlight driving voltage	V_F	-	9.6	-	V	
Backlight driving current	I_F	45	60	75	mA	
Backlight Power Consumption	W_{BL}	-	576	-	mW	
Life Time	-	-	50,000	-		Note 3

Note 1: (Unless specified, the ambient temperature Ta=25℃)

Note 2: The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range is exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

3.3 Driving CTP

Item	Value
Working Voltage	DC 3.3V
Interface	I2C
Support Touch	5 Points

4.Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 ℃. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to $0°$.

| Item	Sym.	Values			Unit	Note
		Min.	Typ.	Max.		
1)Contrast Ratio	C/R	900	1200	-		FIG.1
2)Module Luminance	L	700	800	-	cd/m^2	After TP
3)Response time	Tr+Tf	-	30	40	ms	FIG.2
4)Viewing Angle	θ_T	-	80	-	Degree	FIG.3
	θ_B	-	80	-		
	θ_L	-	80	-		
	θ_R	-	80	-		
5)Chromaticity	Wx	0.275	0.315	0.355		
	Wy	0.303	0.343	0.383		
	Rx	-	-	-		
	Ry	-	-	-		
	Gx	-	-	-		
	Gy	-	-	-		
	Bx	-	-	-		
	By	-	-	-		

◆ Measurement System

Notes:

1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\textbf{Surface Luminance with all white pixels}}{\textbf{Surface Luminance with all black pixels}}$$

2. Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.

3. Response time is the time required for the display to transition from white to black (Rising Time, Tr) and from black to white (Falling Time, Tf). For additional information see FIG 2.

4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

FIG. 1 Optical Characteristic Measurement Equipment and Method
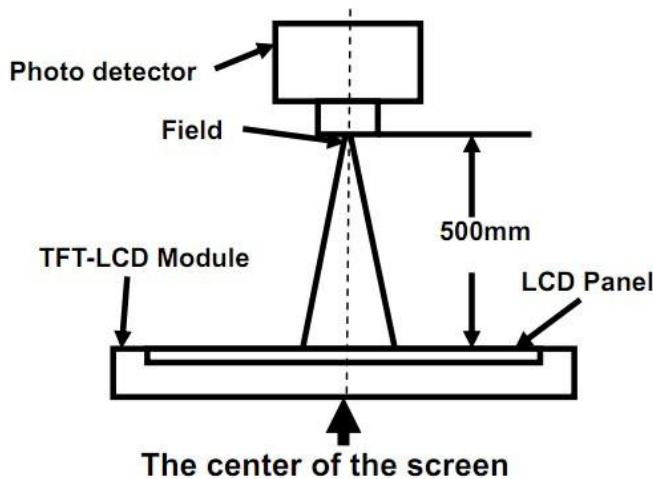
| Item	Photo detector	Field
Contrast Ratio		
Luminance	SR-3A	1°
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

FIG. 2 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".
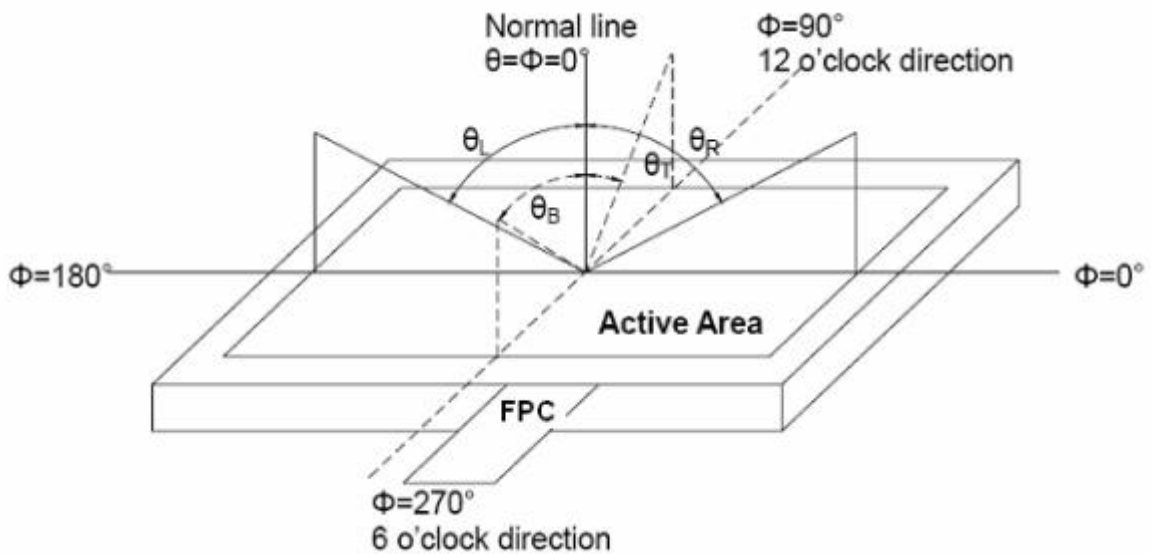
Response Time = Rising Time(Tr) + Falling Time(Tf)

- Rising Time(Tr) : Full White 90% → Full White 10% Transmittance.
- Falling Time(Tf) : Full White 10% → Full White 90% Transmittance.
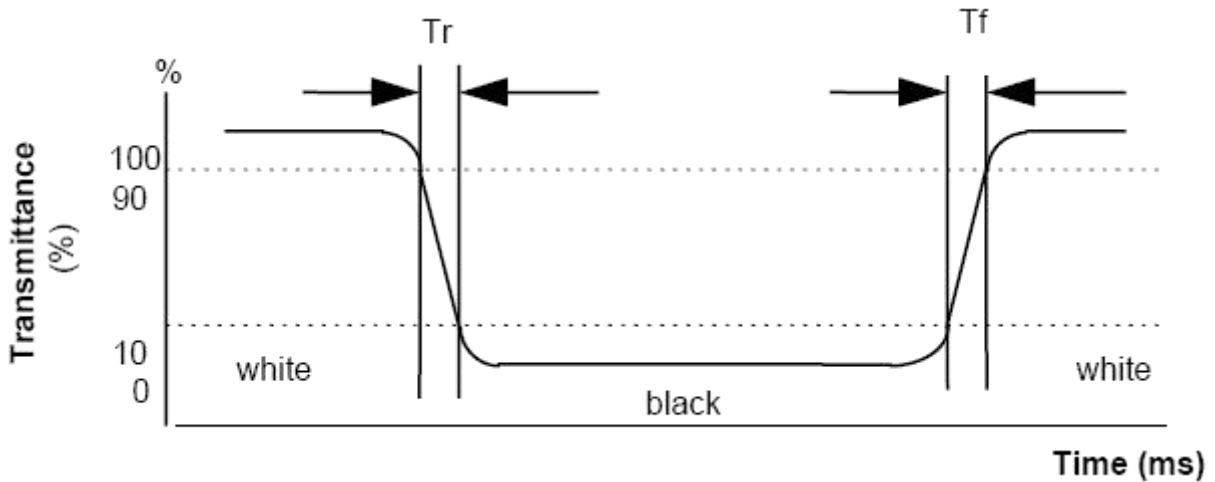
FIG. 3 The definition of Viewing Angle

Use Fig. 1(Test Procedure) under Measurement System to measure the contrast from the measuring direction specified by the conditions as the following figure.

5.Block Diagram

320×3(RGB)×240
Display Panel
(TFT LCD
Transmissive)

Gate
signal
(240)

Source signal (320*3)

Driver & Controller   (All-In-One)
(ST7272A)

R
G
B
&
P
O
W
E
R

Data(R0~R7，G0~G7，B0~B7，)
Power(VDD)

&

Control,Signal(PCLK,HSYNC,VSYNC,DE
,RESET)
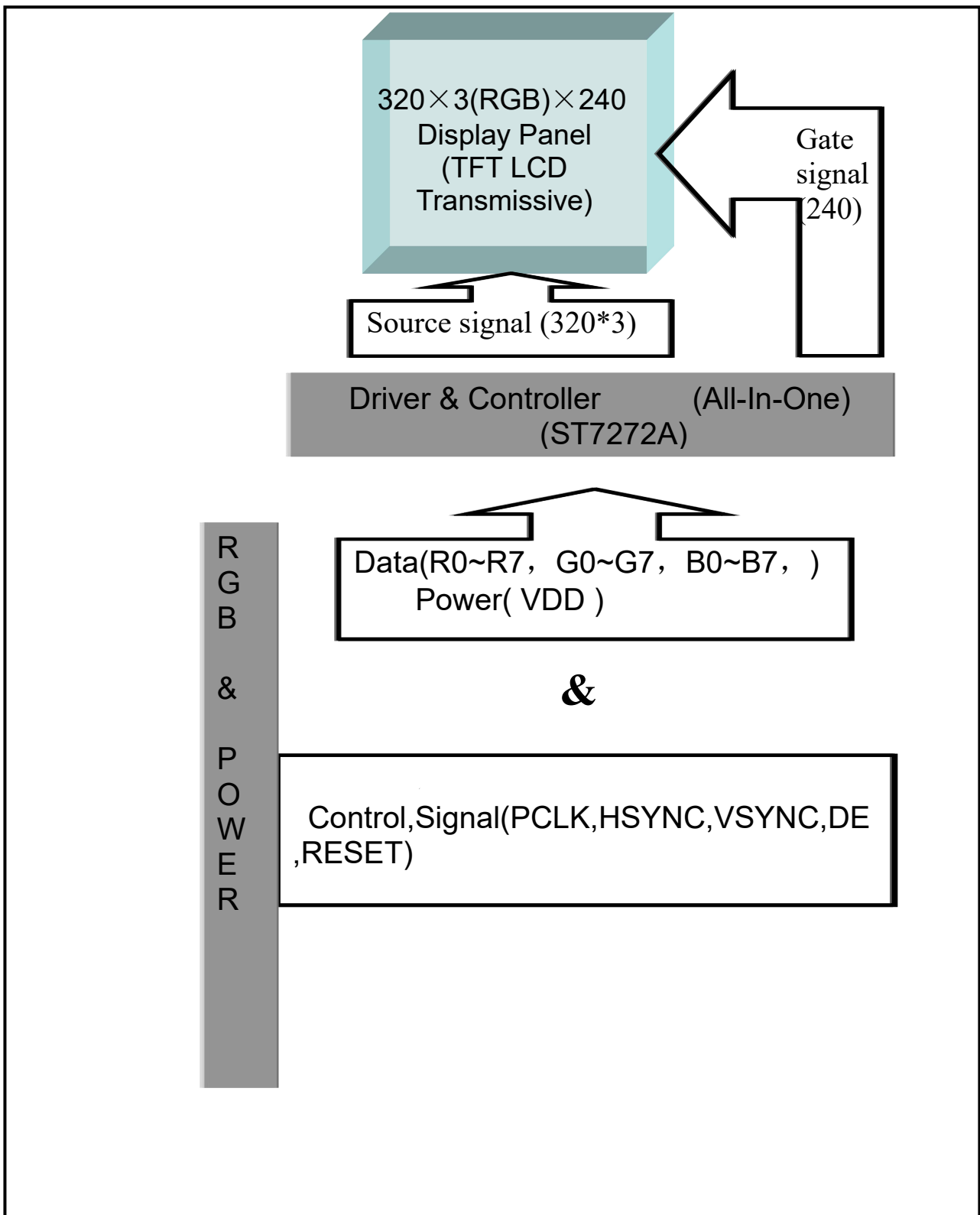
6.Pin Description

6.1 TFT LCD Pin Description

Item	Symbol	I/O/P	Functions	Remarks
1	LED-K	P	B/L Power input PIN cathode	
2	LED-K	P	B/L Power input PIN cathode	
3	LED-A	P	B/L Power input PIN anode	
4	LED-A	P	B/L Power input PIN anode	
5	NC	-	No connection	
6	NC	-	No connection	
7	NC	-	No connection	
8	RESET	I	Reset	
9	NC	I	No connection	
10	NC	I	No connection	
11	NC	I/O	No connection	
12-19	B0-B7	I	Blue Data	
20-27	G0-G7	I	Green Data	
28-35	R0-R7	I	Red Data	
36	HSYNC	I	Horizontal synchronizing signal	
37	VSYNC	I	Vertical synchronizing signal	
38	DCLK	I	Data Clock	
39	NC	-	No connection	
40	NC	-	No connection	
41	VDD	I	Power supply	
42	VDD	I	Power supply	
43	NC	I	No connection	
44	NC	I	No connection	
45	NC	-	No connection	
46	NC	-	No connection	
47	NC	-	No connection	
48	NC	I	No connection	
49	NC	I	No connection	
50	NC	I	No connection	
51	NC	-	No connection	
52	DE	I	Data Enable Signal	
53	GND	I	Ground	
54	GND	I	Ground	

6.2 CTP LCD Pin Description

Pin	Symbol	Description
1	VDD	Working voltage 3.3V
2	Reset	Reset
3	INT	Interrupt
4	SCL	I2C clock
5	SDA	I2C data
6	GND	GND

CTP IC driver source code will be offered separately.

7. Timing Characteristics

7.1. Input Setup Timing Setting

#### 7.1.1. Parallel 24-bit RGB Timing Table

Parallel 24-bit RGB Input Timing Table						
Item		Symbol	Min.	Typ.	Max.	Unit	Note
DCLK Frequency		Fclk	5	6	8	MHz	
DCLK Period		Tclk	125	167	200	ns	
HSYNC	Period Time	Th	325	371	438	DCLK	
	Display Period	Thdisp		320		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	SYNC mode back porch control by H_BLANKING[7:0] setting Thbp= H_BLANKING[7:0]
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period Time	Tv	244	260	289	HSYNC	
	Display Period	Tvdisp		240		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	SYNC mode back porch control by V_BLANKING[7:0] setting Tvbp= V_BLANKING[7:0]
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

7.1.2. SYNC Mode Timing Diagram
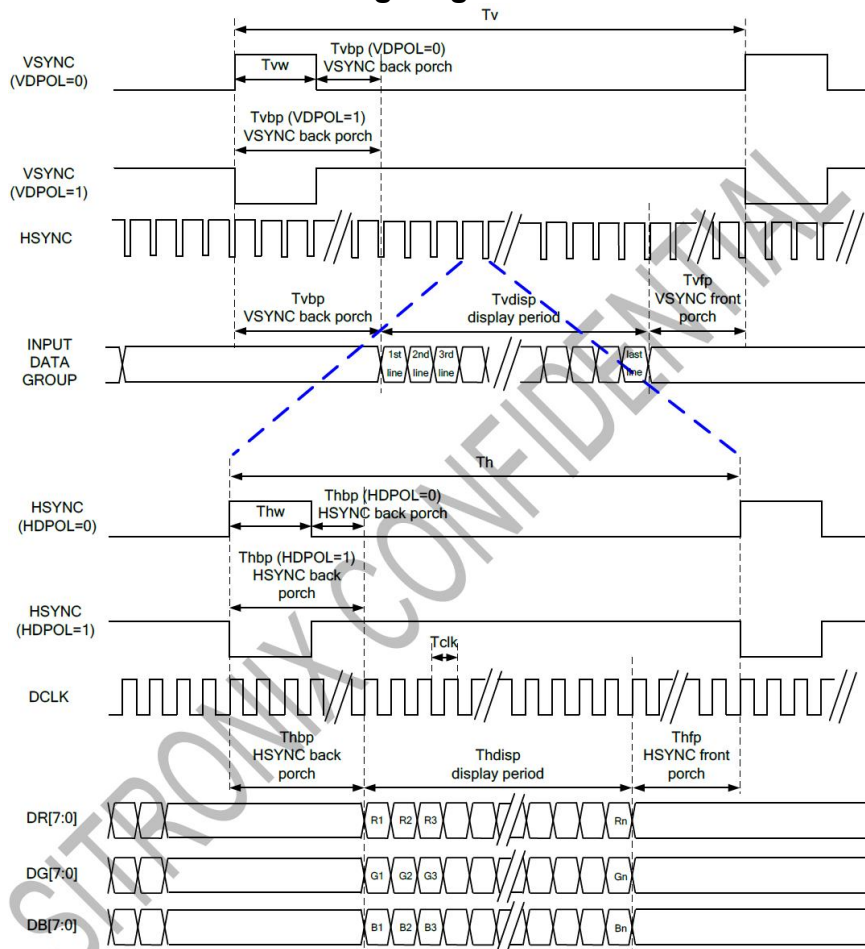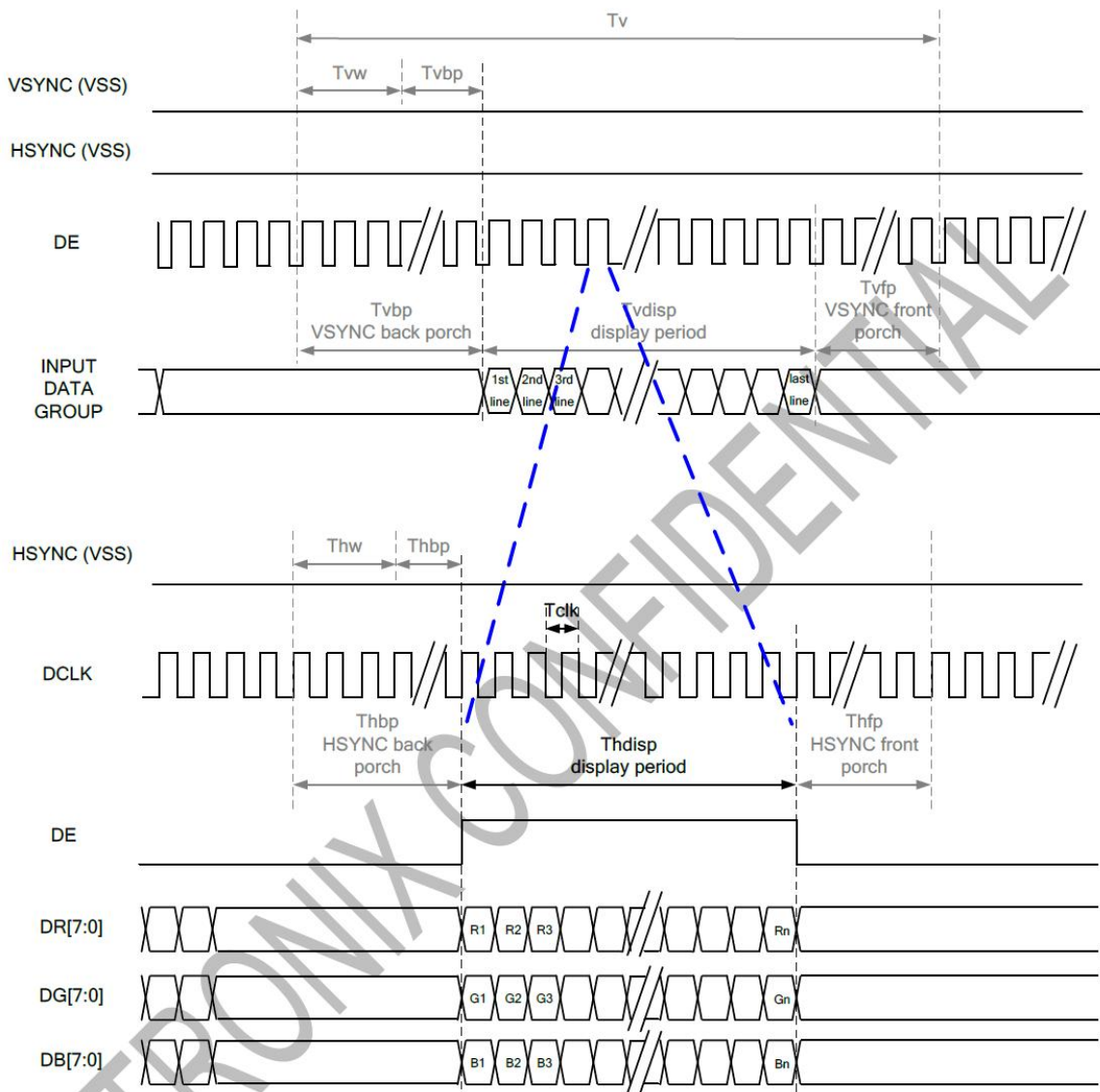
7.1.3. SYNC-DE Mode Timing Diagram
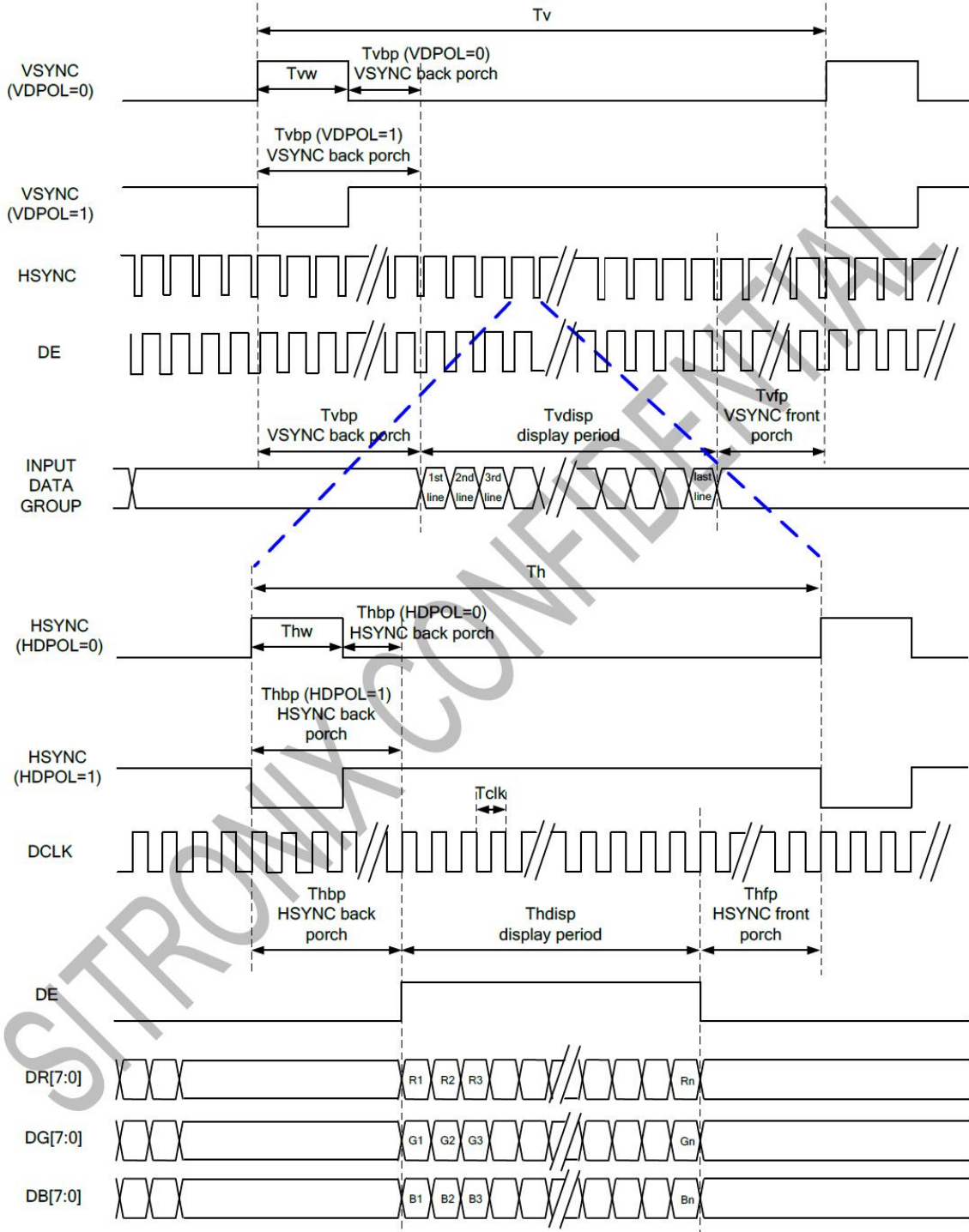
7.1.4. DE Mode Timing Diagram

7.2. RGB Mode Selection Table

RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

7.3. System Bus Timing for RGB Interface
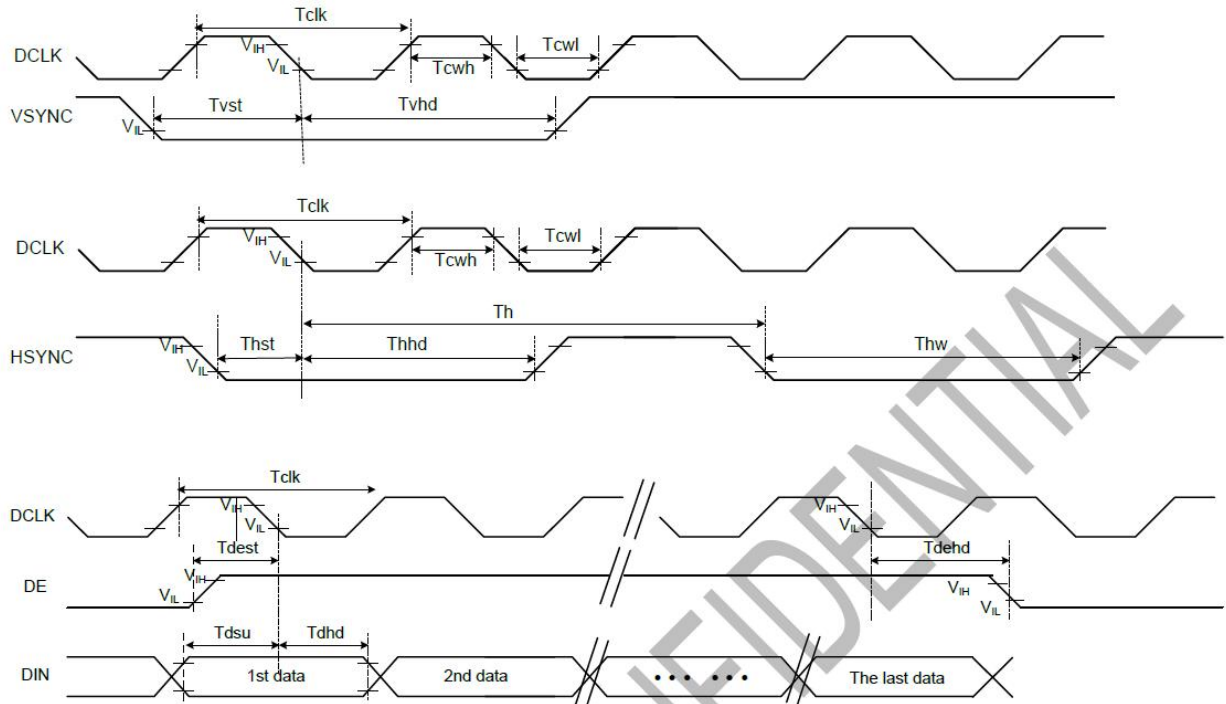
Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	Tclk	40	50	60	%	
HSYNC Width	Thw	2	-	-	DCLK	
HSYNC Period	Th	55	60	65	us	
VSYNC Setup Time	Tvst	12	-	-	ns	
VSYNC Hold Time	Tvhd	12	-	-	ns	
HSYNC Setup Time	Thst	12	-	-	ns	
HSYNC Hold Time	Thhd	12	-	-	ns	
Data Setup Time	Tdsu	12	-	-	ns	
Data Hold Time	Tdhd	12	-	-	ns	
DE Setup Time	Tdest	12	-	-	ns	
DE Hold Time	Tdehd	12	-	-	ns	

7.4. Power ON/OFF Sequence

7.4.1. Power ON Sequence
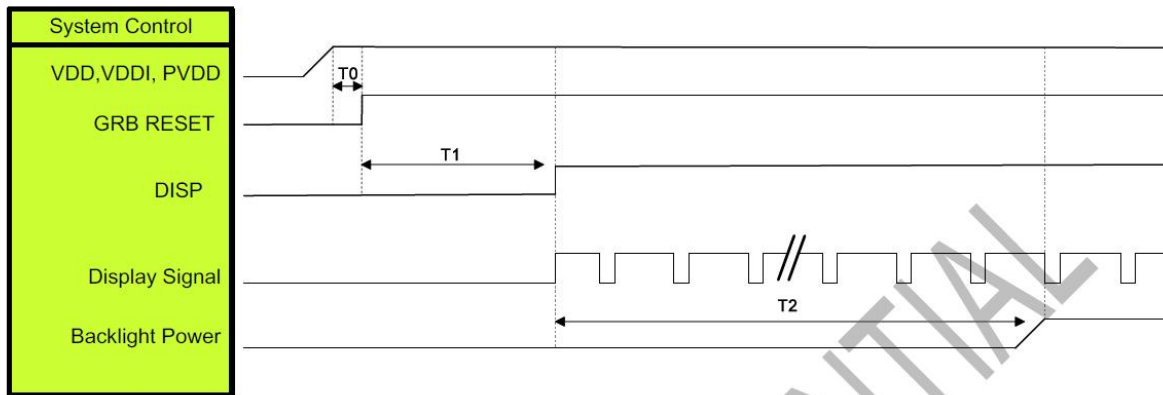
Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

7.4.2. Power OFF Sequence
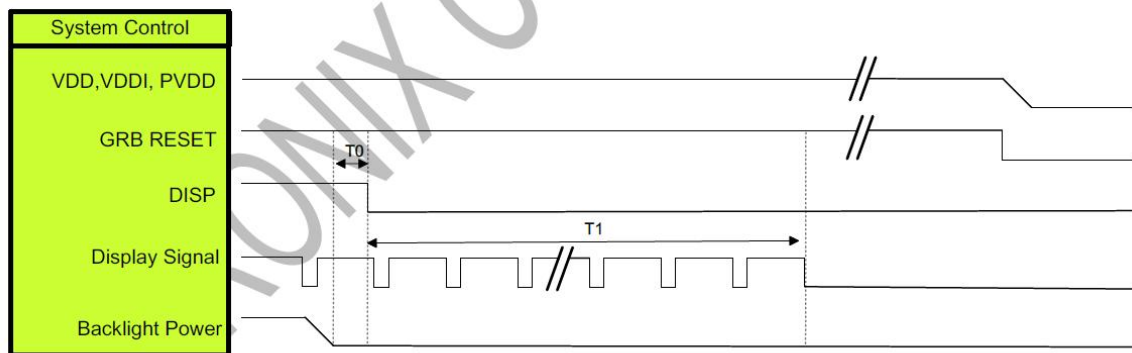
Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

8. Outline Dimension
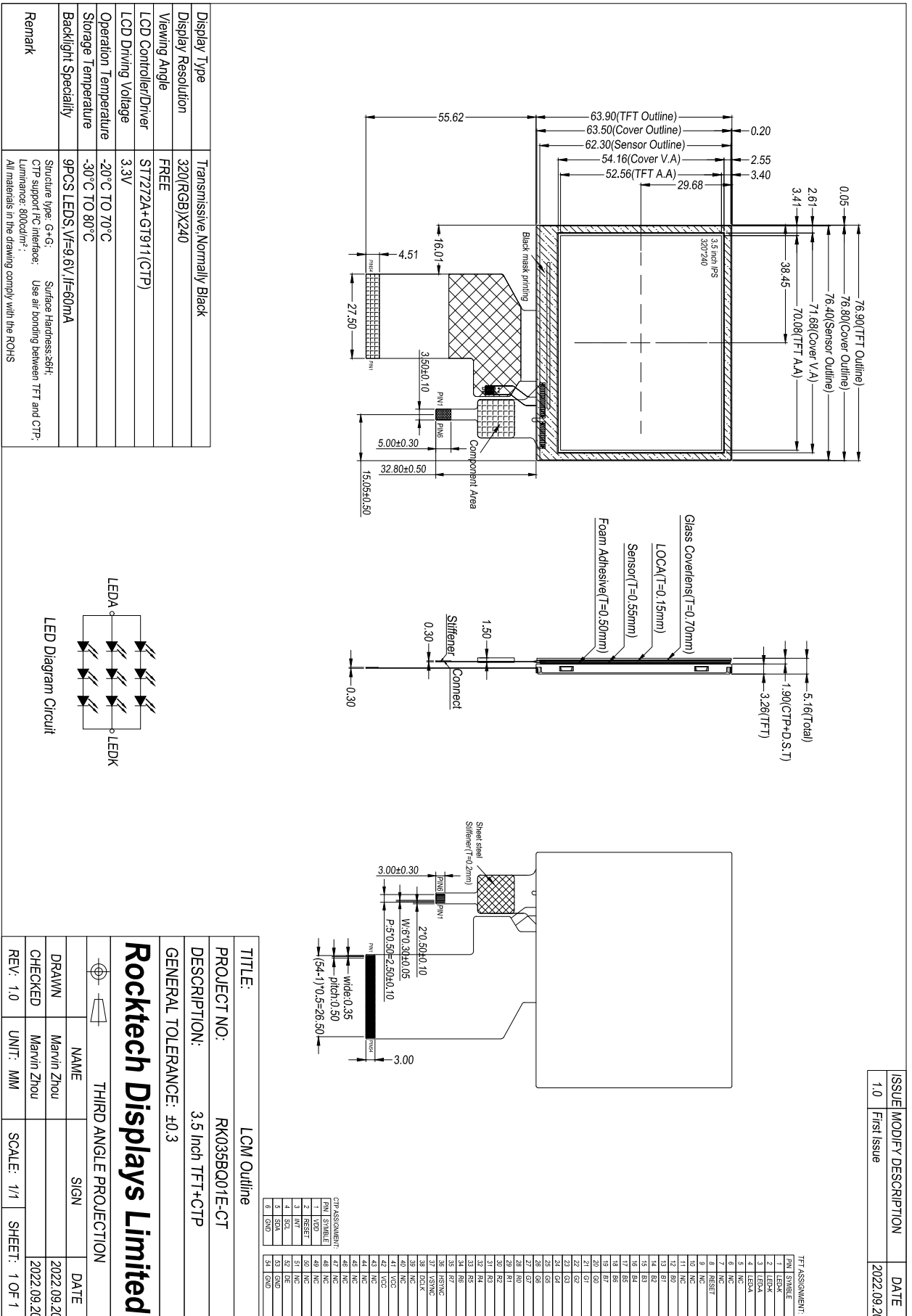
Display Type	Transmissive,Normally Black
Display Resolution	320(RGB)X240
Viewing Angle	FREE
LCD Controller/Driver	ST7272A+GT911(CTP)
LCD Driving Voltage	3.3V
Operation Temperature	-20°C TO 70°C
Storage Temperature	-30°C TO 80°C
Backlight Speciality	9PCS LEDS, Vf=9.6V,If=60mA
Remark	Structure type: G+G; CTP support I²C interface; Luminance: 800cd/m²; Surface Hardness:≥6H; Use air bonding between TFT and CTP; All materials in the drawing comply with the ROHS

LED Diagram Circuit

LEDA

LEDK

Dimension labels (outline view):
- 55.62
- 63.90(TFT Outline)
- 63.50(Cover Outline)
- 62.30(Sensor Outline)
- 54.16(Cover V.A)
- 52.56(TFT A.A)
- 29.68
- 0.20
- 2.55
- 3.40
- 0.05
- 2.61
- 3.41
- 38.45
- 76.90(TFT Outline)
- 76.80(Cover Outline)
- 76.40(Sensor Outline)
- 71.68(Cover V.A)
- 70.08(TFT A.A)
- 4.51
- 16.01
- 27.50
- 3.50±0.10
- 5.00±0.30
- 15.05±0.50
- 32.80±0.50
- Black mask printing
- 3.5 inch IPS 320*240
- Component Area
- PIN54 / PIN1
- PIN1 / PIN6

Cross-section labels:
- Foam Adhesive(T=0.50mm)
- Sensor(T=0.55mm)
- LOCA(T=0.15mm)
- Glass Cover lens(T=0.70mm)
- Stiffener
- Connect
- 0.30
- 1.50
- 0.30
- 5.16(Total)
- 1.90(CTP+D.S.T)
- 3.26(TFT)

Bottom view labels:
- Sheet steel Stiffener(T=0.2mm)
- 3.00±0.30
- PIN6 / PIN1
- 2*0.50±0.10
- W:6*0.3±0.05
- P:5*0.50=2.50±0.10
- wide:0.35
- pitch:0.50
- (54-1)*0.5=26.50
- 3.00
- PIN1
- PIN54

Title Block:

	NAME	SIGN	DATE
DRAWN	Marvin Zhou		2022.09.20
CHECKED	Marvin Zhou		2022.09.20
REV: 1.0	UNIT: MM	SCALE: 1/1	SHEET: 1 OF 1

Rocktech Displays Limited

THIRD ANGLE PROJECTION

TITLE: LCM Outline
PROJECT NO: RK035BQ01E-CT
DESCRIPTION: 3.5 Inch TFT+CTP
GENERAL TOLERANCE: ±0.3

ISSUE	MODIFY DESCRIPTION	DATE
1.0	First Issue	2022.09.20

CTP ASSIGNMENT:

PIN	SYMBLE
1	VDD
2	RESET
3	INT
4	SCL
5	SDA
6	GND

TFT ASSIGNMENT:

PIN	SYMBLE
1	LEDK
2	LEDK
3	LEDA
4	LEDA
5	NC
6	NC
7	NC
8	RESET
9	NC
10	NC
11	NC
12	G0
13	G1
14	G2
15	G3
16	G4
17	G5
18	G6
19	G7
20	B0
21	B1
22	B2
23	B3
24	B4
25	B5
26	B6
27	B7
28	R0
29	R1
30	R2
31	R3
32	R4
33	R5
34	R6
35	R7
36	DCLK
37	HSYNC
38	VSYNC
39	DE
40	NC
41	VCC
42	VCC
43	NC
44	NC
45	NC
46	NC
47	NC
48	NC
49	NC
50	NC
51	NC
52	GND
53	GND
54	GND

9. Reliability and Inspection Standard

No.	Test Item		Test Conditions	Remark
1	High Temperature	Storage	80℃, 120Hr	Note
		Operation	70℃, 120Hr	Note
2	Low Temperature	Storage	-30℃, 120Hr	Note
		Operation	-20℃, 120Hr	
3	High Temperature and High Humidity		40℃, 90%RH, 120Hr	Note
4	Thermal Cycling Test(No operation)		-20℃ for 30min, 70℃ for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note
5	Vibration Test(No operation)		Frequency :10~55 HZ; Stroke :1.5 mm;Sweep:10HZ~55HZ~10HZ; 2hours for each direction of X, Y, Z(6 hours for total)	
6	Package Drop Test		Height:60 cm,1 corner, 3 edges, 6 surfaces	
7	Electro Static Discharge		±2KV,Human Body Mode, 100pF/1500Ω	

Note:
1) Sample quantity for each test item is 5~10pcs.
2) Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

10.PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
- Isopropyl alcohol
- Ethyl alcohol
Do not scrub hard to avoid damaging the display surface.

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
- Water
- Ketone
- Aromatic solvents
Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal

connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

(1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.

(2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.

(3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
-Terminal electrode sections.